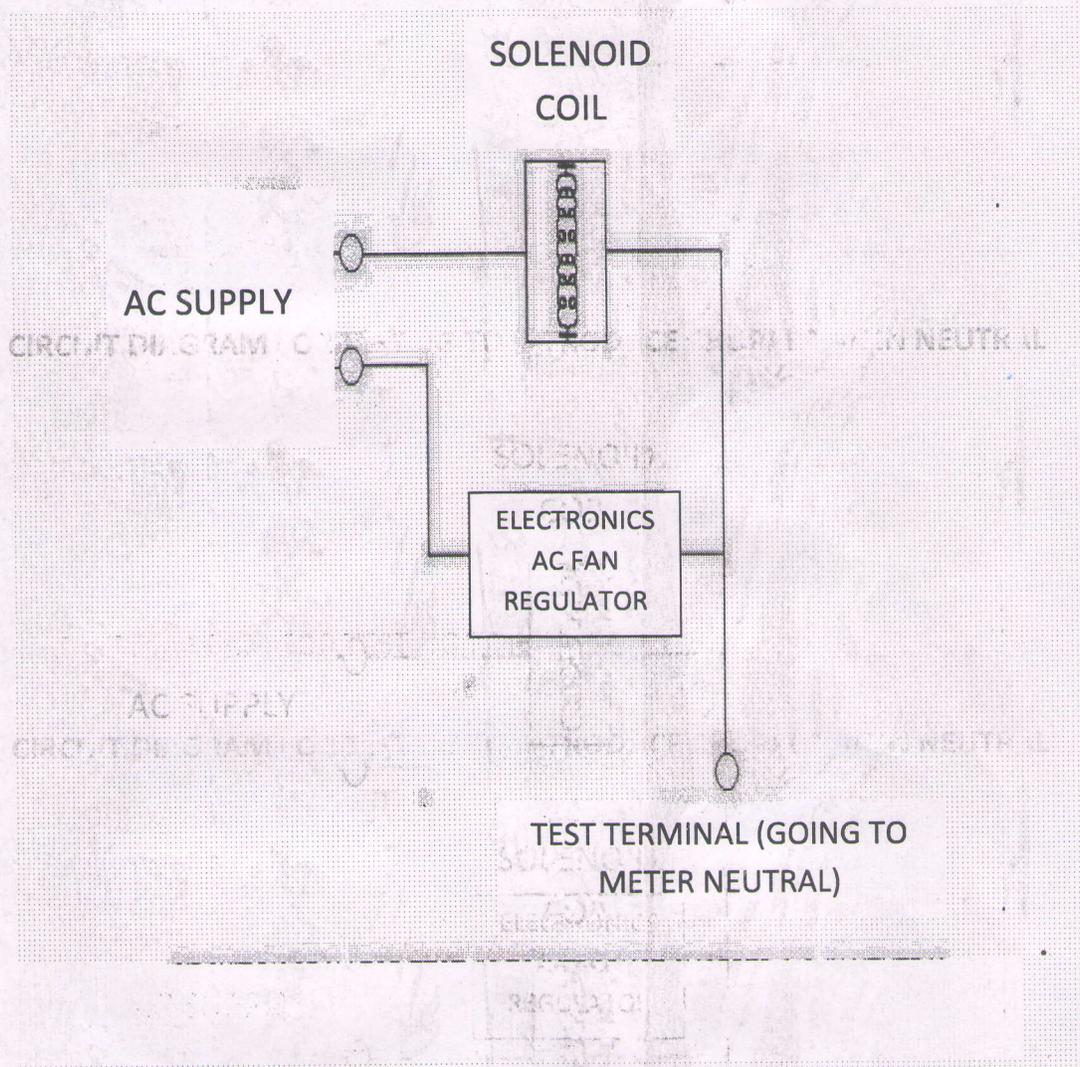


ANNEXURE - III

CIRCUIT DIAGRAM FOR TEST JIG TO INTRODUCE CHOPPED AC IN NEUTRAL



ANNEXURE - IV

CIRCUIT DIAGRAM FOR DC PULSATING SIGNALS IN NEUTRAL

